



PATENT NUMBER

	O.I.P.E.	PATENT DATE
SCANNED T452 Q.A. SS		

APPLICATION NO. 09/899436	CONT/PRIOR D	CLASS 712 710	SUBCLASS 104	ART UNIT 2183 2111	EXAMINER WONG AUVS
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APPLICANTS
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TITLE
System and method for efficiently implementing an electronic device architecture

ISSUING CLASSIFICATION											
ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION											

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent: subsequent to _____ (date) has been disclaimed. _____ <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED:	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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